

FIG. 1

## SDRAM WRITE TIMING

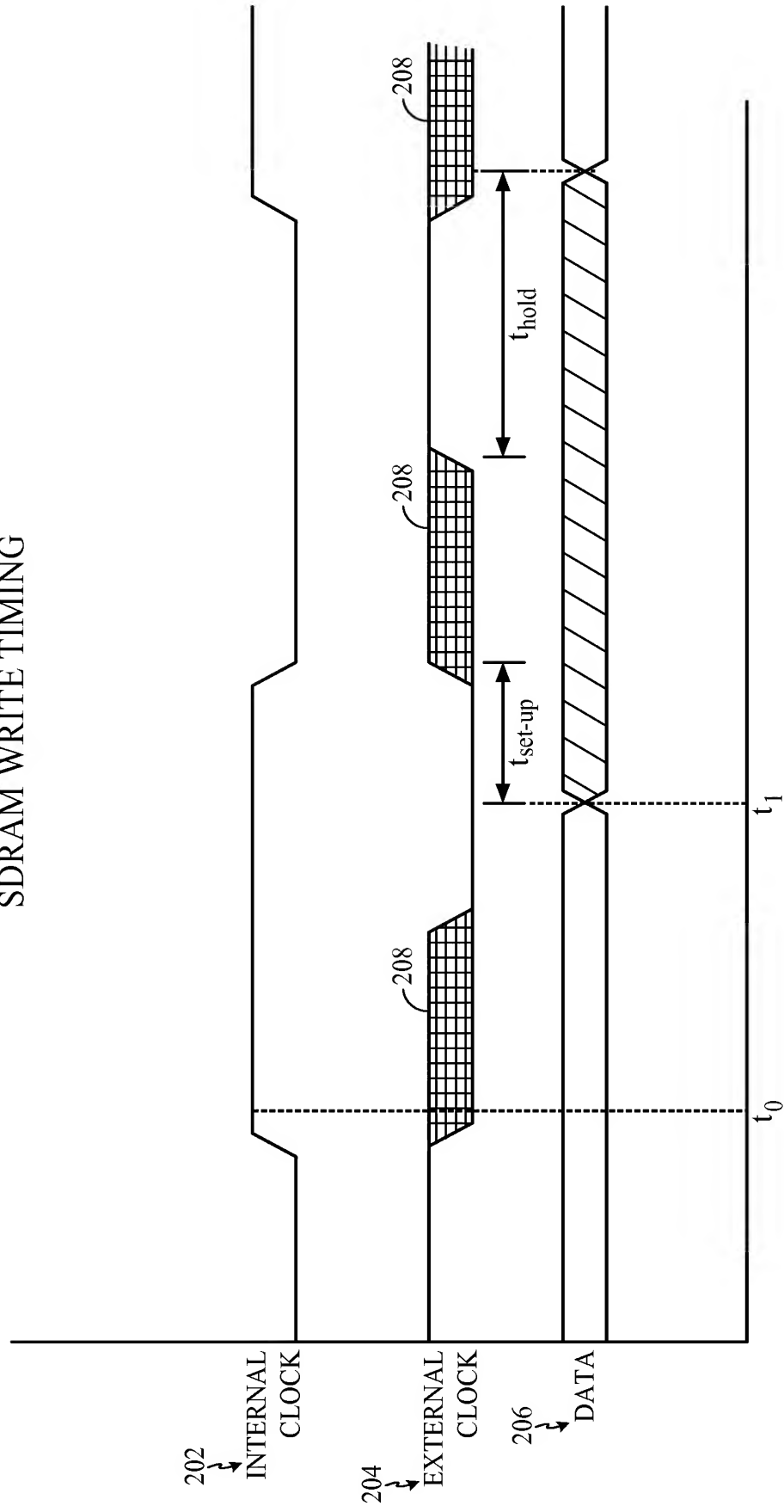


FIG. 2

SDRAM READ TIMING

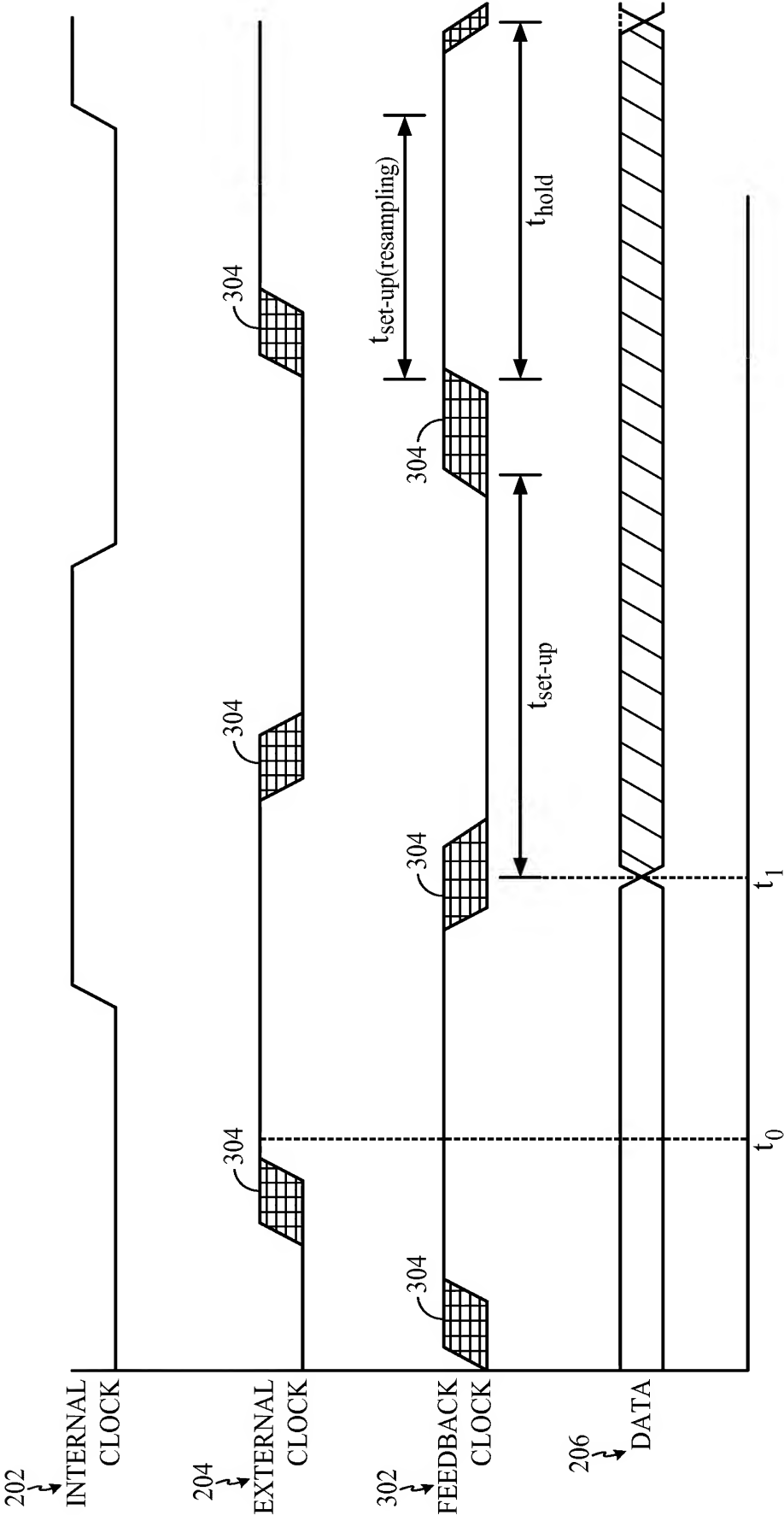


FIG. 3

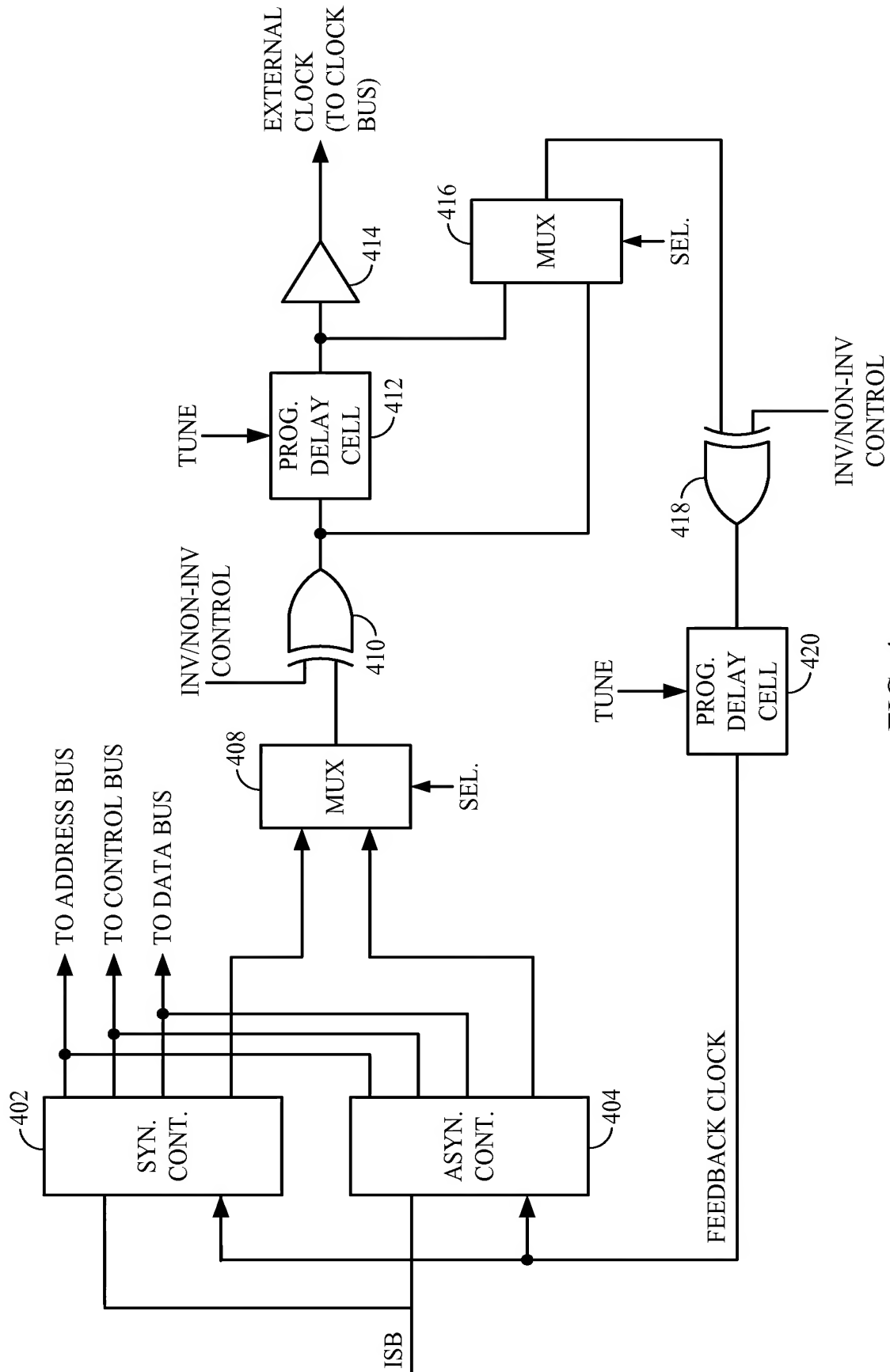


FIG. 4

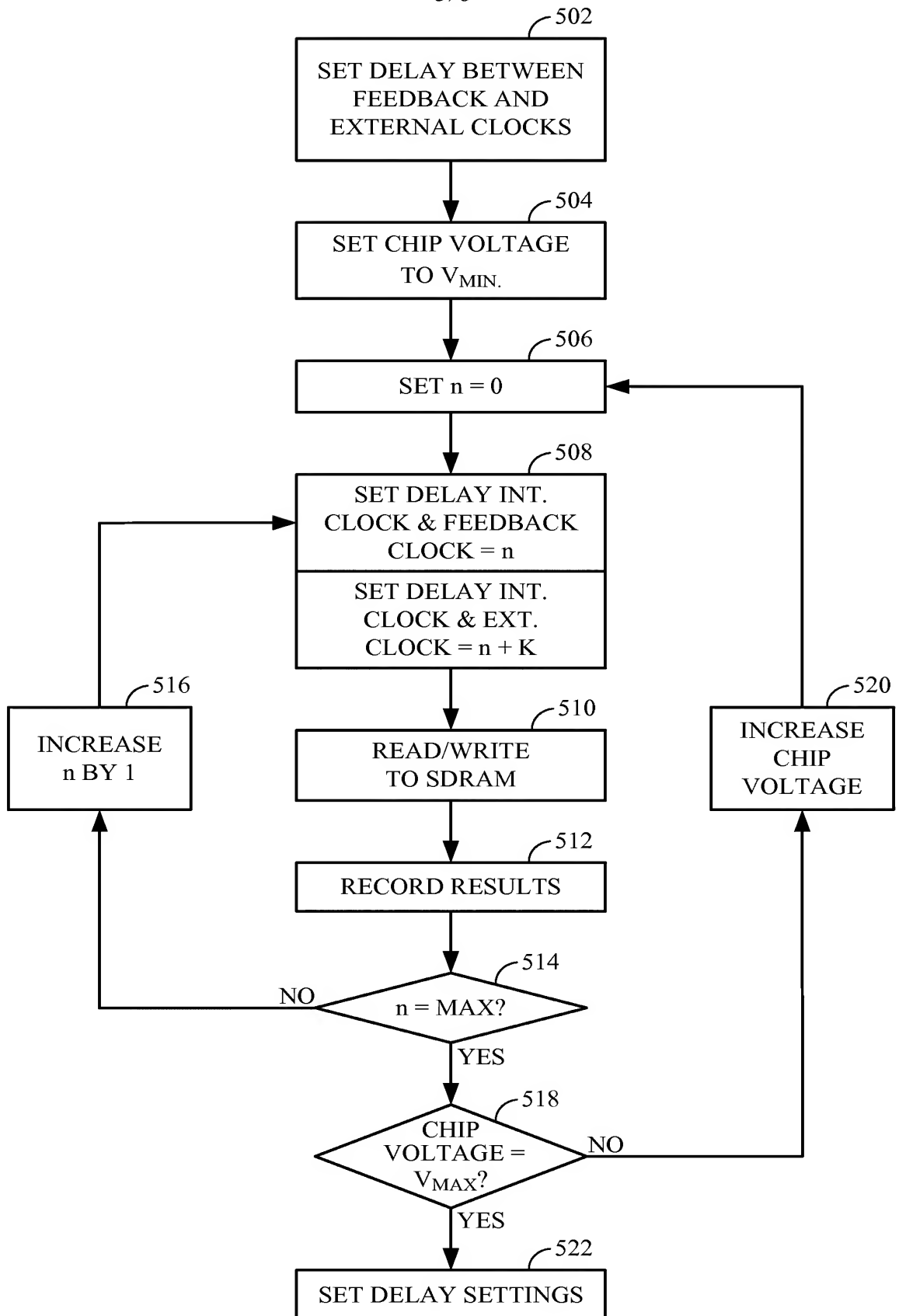


FIG. 5

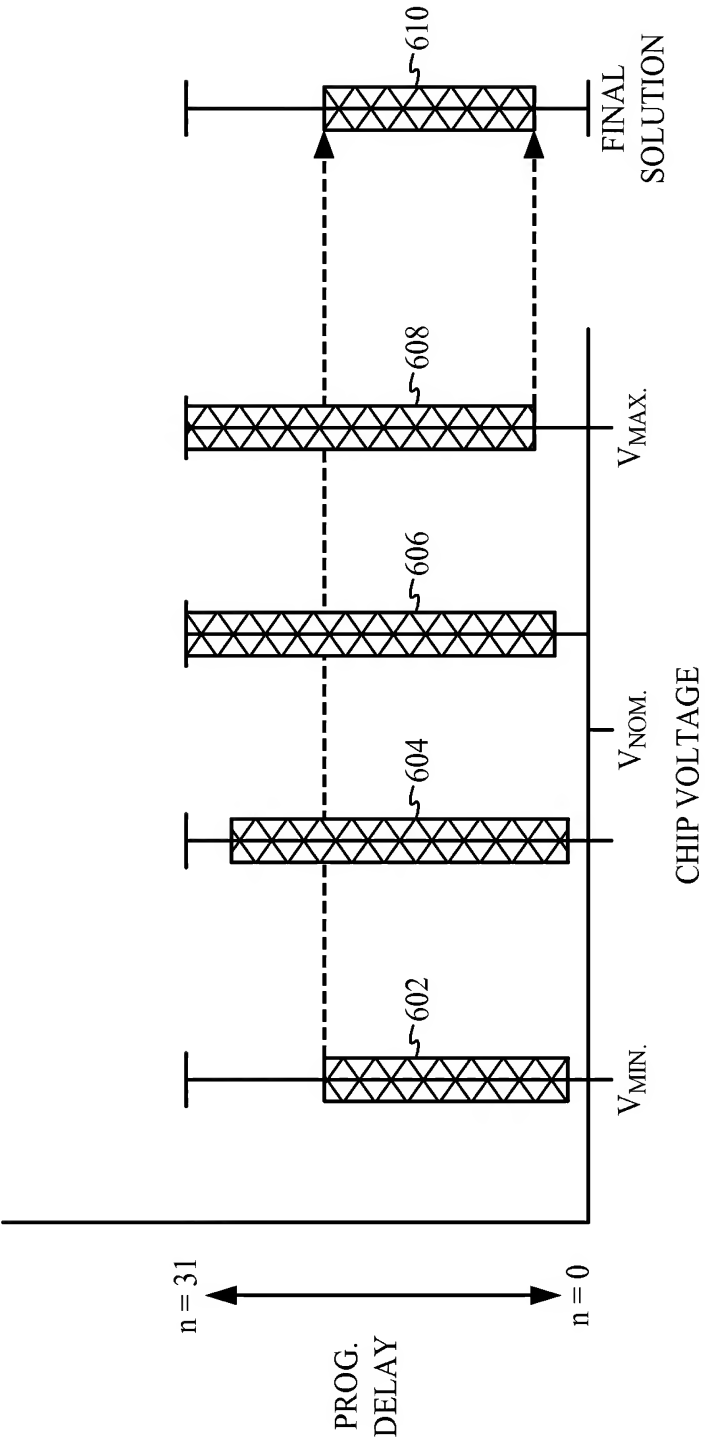


FIG. 6